

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
10 May 2001 (10.05.2001)

PCT

(10) International Publication Number
WO 01/33898 A2

(51) International Patent Classification⁷: **H04Q 11/00**

WARD, Robert, R.; 803 Selkirk Place, Sunnyvale, CA 94087 (US).

(21) International Application Number: PCT/US00/41830

(74) Agents: SCHAAL, William, W. et al.; Blakely, Sokoloff, Taylor & Zafman, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025-1026 (US).

(22) International Filing Date:
2 November 2000 (02.11.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:

60/162,936	2 November 1999 (02.11.1999)	US
60/170,094	10 December 1999 (10.12.1999)	US
60/170,095	10 December 1999 (10.12.1999)	US
60/170,093	10 December 1999 (10.12.1999)	US
60/170,092	10 December 1999 (10.12.1999)	US
60/186,108	1 March 2000 (01.03.2000)	US
60/200,425	28 April 2000 (28.04.2000)	US
09/704,457	1 November 2000 (01.11.2000)	US

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— Without international search report and to be republished upon receipt of that report.

(71) Applicant: XROS, INC. [US/US]; 2305 Mission College Boulevard, Santa Clara, CA 95054 (US).

(72) Inventors: RAMASWAMI, Rajiv; 758 Carlisle Way, Sunnyvale, CA 94087 (US). TABASKA, Steven, T.; 25605 Fernhill Drive, Los Altos Hills, CA 94024 (US).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

WO 01/33898 A2

(54) Title: AN OPTICAL CROSS-CONNECT SWITCHING SYSTEM

(57) Abstract: In general, an optical cross-connect switching system comprising a switch subsystem, an input/output (I/O) subsystem including a plurality of removable, I/O port modules, and a switch control subsystem featuring servo modules. These units collectively operate to provide optical data paths for routing of light signals without conversion from optical to electrical domains and back to optical. Also, the optical cross-connect switching system is scalable because the I/O port modules, servo modules and even features of the switch subsystem may be removed without disruption in system operation.

AN OPTICAL CROSS-CONNECT SWITCHING SYSTEM

CROSS REFERENCE TO RELATED APPLICATIONS

This non-provisional U.S. Patent Application claims the benefit of U.S. Provisional Patent Application No. 60/162,936 entitled "OPTICAL CROSSCONNECT WITH OPTICAL TO ELECTRICAL CONVERTERS" filed on November 2, 1999 by inventor Rajiv Ramaswami; and also claims the benefit of U.S. Provisional Patent Application No. 60/170,094 entitled "OPTICAL CROSSCONNECT WITH BRIDGING, TEST ACCESS AND REDUNDANCY" filed on December 10, 1999 by inventors Rajiv Ramaswami and Robert R. Ward; and also claims the benefit of U.S. Provisional Patent Application No. 60/170,095 entitled "OPTICAL CROSSCONNECT WITH LOW-LOSS BRIDGING, TEST ACCESS AND REDUNDANCY" filed on December 10, 1999 by inventors Steven Clark and Rajiv Ramaswami; and also claims the benefit of U.S. Provisional Patent Application No. 60/170,093 entitled "1+1 OPTICAL PROTECTION USING OPTICAL CROSSCONNECT" filed on December 10, 1999 by inventors Rajiv Ramaswami and Robert R. Ward; and also claims the benefit of U.S. Provisional Patent Application No. 60/170,092 entitled "SIGNALING INTERFACE BETWEEN OPTICAL CROSSCONNECT AND ATTACHED EQUIPMENT" filed on December 10, 1999 by inventor Rajiv Ramaswami; and also claims the benefit of U.S. Provisional Patent Application No. 60/186,108 entitled "1:N PROTECTION BETWEEN CLIENTS AND ALL-OPTICAL CROSSCONNECTS" filed on March 1, 2000 by inventors Kent Erickson, Subhashini Kaligotla, and Rajiv Ramaswami; and also claims the benefit of U.S. Provisional Patent Application No. 60/200,425 entitled "OPTICAL CROSSCONNECT SYSTEM" filed on April 28, 2000 by inventors Rajiv Ramaswami, Steve Tabaska, and Robert Ward.

General Background

Over the last few years, the demand for high-speed communication networks has increased dramatically. In many situations, communication

networks are implemented with electrical interconnections. As desired levels of bandwidth and transmission speed for communication networks increase, it will become more and more difficult for electrical interconnections to satisfy these levels.

5 One difficulty associated with electrical interconnections is that they are sensitive to external electromagnetic interference. More specifically, electromagnetic fields that reside in the vicinity of the interconnection lines induce additional currents, which may cause erroneous signaling. This requires proper shielding, which hampers general heat removal.

10 Another difficulty is that electrical interconnections are subject to excessive inductive coupling, which is referred to as "crosstalk". To alleviate crosstalk, the electrical interconnections must abide by fundamental rules of circuit routing so that they are set at a distance large enough to prevent neighboring signals from having any adverse effect on each other, which would
15 reduce network performance.

Optical interconnections offer a solution to the difficulties affecting conventional electrical interconnections. For example, optical interconnections are not as susceptible to inductive or even capacitive coupling effects as electrical interconnections. In addition, optical interconnections offer increased bandwidth
20 and substantial avoidance of electromagnetic interference. These potential advantages of optics become more important as the transmission rates increase.

Many communications network feature electronic switching devices to arbitrate the flow of information over the optical interconnections. Conventional electronic switching devices for optical signals are designed to include a hybrid
25 optical-electrical semiconductor circuit employing photo detectors, electrical switches, optical modulator or lasers. The incoming optical signals are converted to electrical signals, which are amplified and switched for driving the lasers. One disadvantage associated with a conventional electronic switching device is that it provides less than optimal effectiveness in supporting high data transmission rates
30 and bandwidth.

SUMMARY

In one embodiment, the present invention relates to an optical cross-connect switching system comprising (1) a switch subsystem, (2) a plurality of removable, input/output (I/O) port modules and (3) a switch control subsystem featuring servo modules. These units collectively operate to provide optical data paths for routing of light signals without conversion from optical to electrical domains and back to optical. Also, the optical cross-connect switching system is scalable because the I/O port modules, servo modules and even features of the switch subsystem may be removed without disruption in system operation.

The switch subsystem features at least two optical switch cores each including a number of optical switches such as micro-machined mirrors. The multiple optical switch cores provide redundancy in the event that optical switches for one of the optical switch cores are damaged or inoperable. These optical switch cores are removable without completely disrupting operation of the of the optical cross-connect switching system.

At each port, a removable I/O port module is configured with a splitter and at least two tap couplers. Normally passive in nature, the splitter is configured to produce at least two bridged light signals from an incoming light signal. The tap couplers are used to produce optical tap signals as well as outgoing light signals that are each routed to different optical switch cores. Configurable with a power level lesser than the outgoing light signals, the optical tap signals provide servo modules information for controlling the switch subsystem and monitoring performance variances in the optical data paths. Moreover, each port of an I/O port module includes an optical switch for selectively routing one of the light signals received from the optical switch cores during output operations.

A servo module includes a servo mirror control module and an optical detector module. The servo mirror control module is configured to adjust an optical path of a light signal from a source to a destination. Thus, the monitoring is not performed in the optical data path. The optical detector module,

communicatively and removably coupled to the servo mirror control module, monitors a power level of the light signal to determine whether to adjust the optical path. Moreover, the optical detector module includes a laser to inject a substitute light signal into the optical path. The substitute light signal may be within the same wavelength range as the light signal.

Multiple servo modules are in communication with each other through a servo control module and multiple servo control modules are in communication with each other through network control modules.

Other aspects and features of the present invention will become apparent to those ordinarily skilled in the art upon review of the following description of specific embodiments of the invention in conjunction with the accompanying claims and figures.

BRIEF DESCRIPTION OF THE DRAWINGS

The features and advantages of the present invention will become apparent from the following detailed description of the present invention in which:

Figure 1 is a simplified overview of an exemplary embodiment of an optical cross-connect switching system.

Figure 2 is a first exemplary embodiment of an optical cross-connect switching system of Figure 1.

Figure 3 is an exemplary embodiment of the optical fiber switch matrices forming an optical switch core of Figure 2.

Figure 4 is an exemplary embodiment of mirror arrays forming an optical switch matrix of Figure 3.

Figure 5 is an exemplary embodiment of an I/O subsystem featuring a plurality of I/O port modules.

Figure 6 is an exemplary embodiment of a data path for the transfer of light between I/O port modules and multiple optical switch cores of Figure 2.

Figure 7 is an exemplary embodiment of a control path featuring the interconnections between the I/O port module and servo modules.

Figure 8 is an exemplary embodiment of the I/O port module of Figures 6 and 7 illustrating a data propagation circuit and a control circuit.

Figure 9 is an exemplary embodiment of multiple ports of I/O modules in communication with optical switches controlled by servo modules.

5 Figure 10 is an exemplary embodiment of an I/O port configured as a test access port.

Figure 11 is an exemplary embodiment of a servo module of the optical cross-connect switching system of Figure 1.

10 Figure 12 is an exemplary block diagram of a redundant architecture of the optical cross-connect switching system of Figure 1.

DETAILED DESCRIPTION OF THE INVENTION

15 Herein, the exemplary embodiments of the present invention relate to a scalable, optical cross-connect switching system. The described embodiments should not limit the scope of the present invention, but rather are intended to provide a thorough understanding of the present invention. Certain well-known circuits are not set forth in detail in order to avoid unnecessarily obscuring the present invention.

20 In the following description, certain terminology is used to describe various features of the present invention. For example, a "module" includes a substrate normally formed with any type of material or materials upon which components can be attached such as a printed circuit board or a daughter card for example. Examples of a "component" include an optical switch, a processing unit (e.g., Field Programmable Gate Array "FPGA", digital signal processor, general
25 microprocessor, application specific integrated circuit "ASIC", etc.), splitters and the like. A "splitter" is an optical component that performs a bridging operation on an input light signal by splitting that light signal into two or more output light signals. Each module features one or more interfaces to transport information over a link. A "link" is broadly defined as one or more physical or virtual
30 information-carrying mediums that establish a communication pathway such as, for example, optical fiber, electrical wire, cable, bus traces, wireless channels and

the like. "Information" can be voice, data, address, and/or control in any representative signaling format such as light signals (e.g., light pulses or photons).

I. General Architectural Overview

5

Referring to Figure 1, an exemplary embodiment of a simplified overview of an optical cross-connect switching system 100 is shown. Herein, the optical cross-connect switching system 100 comprises three basic units: a switch subsystem 110, a switch control subsystem 120 and an input/output (I/O) subsystem 130. In one embodiment, the modular architecture of the switch subsystem 110, by a method of having replaceable optical switch cores, provides for switch subsystem maintenance in the event of failure within the switch subsystem 110. It is conceivable that further modularity could be achieved by having replaceable subsections within, thus providing for switch matrix maintenance in the event of failure within a switch matrix itself. The modular architecture of both the switch control subsystem 120 and the I/O subsystem 130, each handling a small number of I/O ports in the system 100, provides scalability to the optical cross-connect switching system 100. Thus, additional I/O ports may be subsequently added to the optical cross-connect switching system 100 by adding or removing input/output (I/O) port modules (described below).

20

The switch subsystem 110 includes optical switches for routing light signals. In one embodiment, the optical switches forming the switch subsystem 110 are micro-machined mirrors; however, it is contemplated that other switch fabrics may be used such as liquid crystal technology. The I/O subsystem 130 receives external light signals 140 and transfers these signals to the switch subsystem 110. The switch control subsystem 120 controls the configuration of the switch subsystem 110 (e.g., mirror orientation) and performs certain monitoring functions. The interconnectivity between the switch subsystem 110, the switch control subsystem 120 and the I/O subsystem 130 includes redundancy so that no equipment failures would cause complete disablement of the system 100.

30

Referring now to Figure 2, a first exemplary embodiment of an optical cross-connect switching system 100 is shown. In general, the optical cross-connect switching system 100 is a matrix-based optical cross-connect with associated I/O port modules. More specifically, the optical cross-connect switching system 100 is collectively formed by a plurality of platforms 205, 206 and 207 in communication with each other, although the implementation of the switching system 100 as a single platform is another embodiment. Herein, each platform 205, 206 and 207 includes a frame 210 (e.g., a rack) that physically supports I/O port modules forming the I/O subsystem 130 as well as servo modules, servo control modules and/or network control modules of the switch control subsystem 120. The modules are arranged either horizontally or vertically within each platform 205, 206 and 207 and can be individually removed or installed without interfering with immediately adjacent modules. In addition, the frame 210 may also physically support one or more optical switch cores, which may also generally be referred to as "switch fabric," of the switch subsystem 110.

As shown in this embodiment, the first platform 205 comprises (i) a plurality of I/O port modules 215 associated with the I/O subsystem 130 of Figure 1, (ii) a plurality of servo modules 225 and a management control subsystem (MCS) 235 associated with switch control subsystem 120 of Figure 1, and (iii) a first (primary) optical switch core 240 associated with switch subsystem 110 of Figure 1. Similarly, the second platform 206 comprises a plurality of additional I/O port modules 245, a plurality of (redundant) servo modules 250, a management control subsystem 255, and a second (redundant) optical switch core 260. The third platform 207 comprises a plurality of servo modules 265 that control various mirrors of the first and second optical switch cores 240 and 260, which correspond to additional ports associated with I/O port modules 245. Additionally, a light path test signal generator(s), a light path signal monitor(s), circuit breakers and/or alarm visual indication 270 may be located within the third platform 207. For clarity, the elements forming the first platform 205 are described since these elements may be found in the second and/or third platforms 206 and 207.

As shown in both Figures 2-4, the first optical switch core 240 includes a first optical switch matrix 241 and a second optical switch matrix 242. These matrices 241 and 242 are collectively positioned to route light signals 250 between a port of a source I/O port module 215_s ("s" is a positive whole number) and a port of a destination I/O port module 215_d ("d" is a positive whole number), both modules located in any of the platforms 205, 206 and 207 as shown in detail in Figure 3. Although a two-bounce routing technique is shown, it is contemplated that a three-bounce routing technique may be used in which the "second bounce" mirror, as optionally shown through dashed line, is positioned to assist in routing light signals from one optical switch matrix to another.

As shown in Figure 4, one embodiment for each of the optical switch matrices 241 and 242 includes multiple arrays 300 of micro-machined mirrors. Each mirror (e.g., mirror 310) features a mirrored surface 311 and torsional flexures 320 and 330 that enable the mirror 310 to adjust its physical orientation to reflect incoming light signals in any selected direction. Herein, both the first and second optical switch matrices 241 and 242 include Q micro-machined mirrors, where "Q" is less than or equal to the maximum number of I/O ports that can be supported by the optical cross-connect switching system 100. For this embodiment, "Q" is greater than or equal to 64 but less than or equal to 1152 ($64 \leq Q \leq 1152$). However, the present invention is not limited to any maximum number of mirrors or I/O ports. It is contemplated, however, that the number of mirrors employed within the first and second optical switch matrices 241 and 242 may differ.

As generally shown in Figures 2, 5 and 6, the plurality of I/O port modules 215 features two groups 216 and 217 of I/O port modules. Each group, such as group 216 or 217 for instance, includes up to seventy-two (72) quad-port I/O port modules as shown in Figure 5 that receive power from one or more power supply modules denoted herein as "PSM". The components forming an I/O port module is described below and shown in Figures 8 and 9. Thus, each I/O port module, such as I/O port module 215_s for example, features an external interface 400 for a plurality of I/O ports 410 (e.g., four I/O ports). An I/O port 410 features a duplex

socket that is adapted to receive a duplex pair of optical fiber links, one optical fiber link routes a light signal to the I/O port 410 while the other routes light signals from the I/O port 410. This support bi-directional optical connections. There is a small percentage (e.g., less than 15%) of these I/O ports, however, that
5 may be assigned as test access ports as described below.

Moreover, as shown in Figure 6, upon receiving an incoming light signal over an optical fiber link 420, the I/O port module 215_s performs a bridging operation by splitting the incoming light signal into multiple (two or more) bridged light signals for routing to the first and second optical switch cores 240
10 and 260. The bridged light signals are routed through an internal optical interface 425 featuring optical fiber ribbon links 430 and 440. For this embodiment, the "optical fiber ribbon links" are ribbon cables having multiple optical fiber lines (e.g., two lines from each I/O port). The first optical switch core 240 provides a primary optical path. The second optical switch core 260 provides a redundant
15 optical path in the event the first optical switch core 240 is not operating properly. The optical switch cores 240 and 260 route the bridged light signals to a selected port of a destination I/O port module (e.g., I/O port module 215_d) via optical fiber ribbon links 450 and 460.

Upon receiving light signals from both the first and second optical switch
20 cores 240 and 260, the I/O port module 215_s provides small percentage optical tap signals of the received light paths to the respective servo modules, which in turn determine light signal quality. The respective servo modules will convey light signal quality for each respective light path to the I/O port module, using a digital protocol over an electrical communication link 505 to the I/O port module as
25 shown in Figure 7. The I/O port module 215_s will in turn, determine (i.e. select) which light signal has the higher signal quality and outputs that signal via interface 400. In most cases, the signal quality of the two light paths presented to the I/O port module will be of the same signal quality and may have a relatively low optical loss of approximately seven decibels (7 dB) or less.

30 Referring now to Figures 2 and 7, each servo module 225 is configured to receive optical tap signals from one or more I/O port modules. Herein, servo

module 225_i is configured to receive optical tap signals via link 500 from I/O port module 215_s. These optical tap signals provide feedback to indicate a percentage of the bridged light signals and also allow for light to be injected under certain conditions. In response to receiving optical tap signals via link 500, the servo module 225_i provides mirror control signals over link 510 to the first optical switch core 240. The mirror control signals are routed via a unique communication path to an optical switch (e.g., a micro-machined mirror) and are associated with the port of the I/O port module 215_s through which the incoming light signal was routed. The mirror control signals are used for proper adjustment of the physical orientation of the mirror.

The I/O port module 215_d provides optical tap signals over link 530 to servo module 225_j. In response to receiving the optical tap signals from I/O port module 215_d, the servo module 225_j provides mirror control signals via link 540 to the first optical switch core 240. The mirror control signals are routed via a unique communication path to a micro-machined mirror associated with a selected port of the I/O port module 215_d from which the light signal would be output. Herein, sensing the optical tap (feedback) signals, the servo module 225_j determines the light signal quality and conveys light signal quality information for each light path using a digital protocol over (electrical) link 535. Thereafter, the I/O port module 215_d chooses the selected port (i.e. port having the best light signal quality).

Collectively, the optical tap signals, mirror control signals and light signal quality information, which are routed over links 500, 510, 530, 540, 505 and 535, are used by servo modules 225_i and 225_j for adjustment of the physical orientation of mirrors to make a connection between I/O port module 215_s and 215_d.

Additionally, I/O port modules 215_s and 215_d also transfer optical tap signals via links 520 and 550, respectively. Similar to the above description, these optical tap signals establish the redundant optical path by altering the physical orientation of one or more micro-machined mirrors of the second optical switch core 260 using mirror control signals over links 560 and 570 and light signal quality information via links 525 and 555.

In the event that no optical power is presented to the I/O port module 215_s, a substitute light signal may be injected from the servo module 225_i via link 500. An alignment laser may be used as shown in Figure 11 described below. This process of light substitution allows for connection establishment and verification when no input light is present to the I/O port module 215_s. The substitute light source can be within the same wavelength range (e.g. 1100 nanometers "nm" - 1700 nm) as the allowed input light signal range. In one embodiment, the light source or method of injection would be chosen to not interfere with attached equipment's select operational wavelength range. Choosing a different wavelength source on the servo module and/or a wavelength specific splitter and/or filter on the I/O port module could do this particular embodiment.

The management control subsystem 235 (see Figure 2) enables communications between two or more servo modules placed within the same or different platforms. The management control subsystem 235 includes at least one servo control module 236 and an optional network control module 238. In one embodiment, the servo control module (SCM) 236 ensures communication between at least servo modules 225_i and 225_j that control mirrors associated with the first optical switch core 240. The network control module (NCM) 238 manages the execution of connection configurations for the whole cross-connect switching system and ensures communications between multiple servo control modules 236 and 237. The same architecture is used to control optical switches within the second optical switch core 260 as shown.

II. General Architecture of the I/O Port Modules

Referring now to Figures 8 and 9, an exemplary embodiment of an I/O port module (e.g., I/O port module 215_s) and its communications over optical switch cores 240 and 260 is shown. I/O port module 215_s includes a data propagation circuit 600 for each I/O port and a control circuit 670. Thus, in the event that the I/O port module 215_s is configured with four I/O ports, four data propagation circuits are implemented on the I/O port module 215_s as represented.

Only the data propagation circuit 600 for one of the I/O ports of I/O port module 215_s (e.g., ith I/O port) is shown in detail for clarity sake.

In one embodiment, the data propagation circuit 600 comprises an optical switch 610, a (passive) splitter 620 and a plurality of tap couplers 630₁-630₄. The plurality of tap couplers 630₁-630₄ correspond to the pairs of optical fibers found in optical fiber ribbon links 430 and 440. The control circuit 670 comprises a programmable memory 680, a processing unit 685 and status identification components 690.

As shown, each port of the I/O port module 215_s supports full-duplex communications. Thus, an incoming light signal 606 received over port 605 is routed to the splitter 620. The splitter 620 effectively performs a bridging operation by splitting the incoming light signal 606 into bridged light signals 625, which collectively have the same power level (energy) as the light signal 606. In one embodiment, when the splitter 620 is a 50/50 splitter, the bridged light signals 625 have equal power levels. However, it is contemplated that splitter 620 may produce bridged light signals 625 having disproportionate power levels.

The bridged light signals 625 are routed through the tap couplers 630₁ and 630₂. Attached to servo module 225_i and servo module 225_{i+1} via optical tap links 500 and 520, the tap couplers 630₁ and 630₂ are used to monitor the power level of light signals 635 and 636 propagating through optical fiber ribbon links 430 and 440 (referred to as "outgoing light signals"). This enables the servo modules 225_i and 225_{i+1} to verify the connectivity of the splitter 620 to optical fiber ribbon links 430 and 440 and to detect unacceptable variances in optical performance of the light signal. As shown for this embodiment, the tap couplers 630₁ and 630₂ may separate the bridged light signals into signals having disproportionate power levels in order to maximize the power levels of the outgoing light signals propagating through optical fiber ribbon links 430 and 440. For example, where the tap couplers 630₁ and 630₂ may operate as 90/10 splitters, the outgoing light signals 635 and 636 have ninety (90%) of the total power level of the bridged light signal while the tap optical signals 640 and 641 have only ten percent (10%).

Referring to Figure 8, tap couplers 630₃ and 630₄ are configured to receive incoming light signal 650 and 655 via optical fiber ribbon links 430 and 440, respectively. The tap couplers 630₃ and 630₄ effectively separate the light signals 650 and 655 into corresponding pairs of light signals having disproportionate power levels (e.g., signals 661, 662 and 663, 664). Signals 662 and 664 having the lower power level are provided to the servo module 225_i and servo module 225_{i+1} via links 500 and 520 for monitoring the power levels of the light signals 661 and 663, without the light signals 661 and 663 experiencing substantial signal degradation. The signals 662 and 664 may be light signals that undergo O/E conversion at the I/O port module 215_s or at the servo modules 225_i and 225_{i+1} as shown in Figure 11. The tap couplers 630₃ and 630₄ are shown as 90/10 splitters; however, tap couplers 630₃ and 630₄ may be any selected ratio, including 50/50.

The light signals 661 and 663 are routed to the optical switch 610 of a destined I/O port. The control circuit 650 on the I/O port module 215_s determines which of the pair of light signals 661 and 663 has the best signal quality based on conveyed light signal quality information from the servo modules via links 505 and 525 as briefly described below. Parameters used to determine light signal quality include measured optical signal intensity/power, extinction ratio, and the like. The light signal quality information to the I/O port module may be conveyed as failed due to the servo module service operations, high bit error rate, an external light path has failed, and the like. The light signal 661 or 663 with the best signal quality is output through the I/O port 605. Of course, it is contemplated that the light signal output operations described for I/O port i are applicable to I/O port j as shown.

It is contemplated that an I/O port of the I/O port module 215_s may be configured as a test access port. A "test access port" is an I/O port that is used for monitoring light signals routed through another port. Normally, the test access port receives a portion of the power level of a light signal routed through a selected optical switch (e.g., micro-machined mirror). For example, as shown in Figure 10, an I/O port 218 of the I/O port module 215_s is configured for coupling with a monitoring device 219 (e.g., a bit error rate "BER" monitor in combination

with an optical-electrical "O/E" converter, etc.) to monitor a power level of a light signal routed to the i^{th} I/O port from an optical switch.

Referring back to Figure 8, the control circuit 670 comprises the programmable memory 680 in communication with the processing unit 685 (e.g., FPGA). The programmable memory 680 contains software and other information used by the processing unit 685 to provide selection of the best quality signal based on digital electrical signaling from servo module 225_i and servo module 225_{i+1} over links 505 and 525, respectively. Also, programmable memory 680 includes information used by the processing unit 685 to control the state of the status identification components 690 (e.g., light emitting diodes "LEDs"). The state of the status identification components 690 identifies (1) whether each I/O port is operational and/or (2) whether the I/O port module is operational. The processing unit 685 is further in communications with optical switches of each data propagation circuit employed in the I/O port module 215_i in order to receive switch status signals and provide switch control signals. As shown for clarity, processing unit 685 provides optical switch 610 with switch control signals for receiving switch status signals and selecting either light signal 661 or light signal 663.

III. General Architecture of the Servo Modules

Referring now to Figure 11, an exemplary embodiment of the servo module (e.g., servo module 225_i) is shown. In one embodiment, the servo module 225_i comprises two separate modules in communication over connectors 705 and 790. These separate modules are referred to as an "optical detector module" 700 and a "servo mirror control module" 750.

The optical detector module 700 comprises a first processing unit 710, memory 715, a plurality of detection/modulation (DM) circuits 716 and status identification components 717. As shown, the optical detector module 700 features sixteen (16) DM circuits 716 to support four (4) quad-port I/O port modules. Each

DM circuit 716 includes an analog-to-digital (A/D) converter 720, a laser 725, optical-electrical (O/E) detectors 730 and 731, and optional amplifiers 735 and 736.

The servo mirror control module 750 comprises a second processing unit 755, a memory 760, a plurality of mirror signal detection and generation (SDG) circuits 761, a third processing unit 775 and status identification components 795. The SDG circuits 761 correspond in number to the DM circuits 716 of the optical detector module 700. Each SDG circuit 761 features an A/D converter 765, a digital-to-analog (D/A) converter 770, hinge position sensors 780-781 and high voltage (HV) mirror drivers 785-786.

As shown in Figure 11, the optical detector module 700 is removably coupled to the servo mirror control module 750. This allows the optical detector module 700 to be "hot swapped" from a backplane, which features connectors 705 and 790 connecting the optical detector module 700 to the servo mirror control module 750, without disrupting the servo mirror control module's 750 ability to hold the mirrors in their existing positions for an extended period of time. This "hot swapping" of the optical detector module 700 allows for repair or upgrade of the optical detector module 700. Optical detector module 700 receives optical tap (feedback) signals 640 and 662 from one or more I/O port modules (e.g., I/O port module 215, via link 500) and can transmit optical control signals 726 from the laser 725 for alignment of light signals transferred between two I/O port modules. The optical tap signal 640 is based on an input light signal that is routed to the switch fabric.

More specifically, with respect to servo module 225_i, the O/E detectors 730 and 731 are coupled to tap couplers 630_i and 630_j of Figures 8-9. More specifically, the O/E detectors 730 and 731 are configured to detect incoming, optical tap signals 640 and 662, convert the optical tap signals 640 and 662 into corresponding electrical control signals measuring a power level of the outgoing light signal, and optionally route the electrical control signals to corresponding amplifiers 735 and 736. The (amplified) electrical control signals are provided to the A/D converter 720. The A/D converter 720 converts the electrical control

signals into measured power sense signals 644 of a digital form. The measured power sense signals 644 are provided to the first processing unit 710.

Herein, the first processing unit 710 may perform a number of operations based on the electrical control signals such as threshold crossing, LOS integration, input/output power ratio analysis and the like. Software and other information
5 necessary for performing these operations may be obtained from the memory 715 by the first processing unit 710. Herein, memory 715 can be non-volatile memory such as non-volatile random access memory, electrically erasable programmable read only memory (EEPROM) and the like.

The optical detector module 700 includes multiple status identification components 717 (e.g., light emitting diodes "LEDs"). A first LED 718 identifies whether any operational faults associated with the servo module 225, have
10 occurred. A second LED 719 indicates when the optical detector module 700 is in service.

Referring still to Figure 11, in this embodiment, the servo mirror control module 750 comprises the second processing unit 755 that is coupled to both the first processing unit 710 and the third processing unit 775. For instance, in order to adjust the switch fabric in response to the measured power sense signals 644, the second processing unit 755 receives information representative of the
20 measured power sense signals from the first processing unit 710 via connectors 705 and 790. The second processing unit 755 further receives information representative of measured power sense signals for the light signal at a targeted I/O port. This information is provided by the SCM 236 over link 580 via the third processing unit 775. This assists in reducing errors in adjusting the torsional
25 flexures of the mirrors.

Upon receipt of these measured power readings, the second processing unit 755 controls a particular SDG circuit corresponding to a mirror associated with the I/O port over which the tapped light signal was routed. The control involves slight mirror orientation adjustments if the power level readings differ
30 substantially.

In particular, a first hinge position sensor 780 senses a position of a mirror via link 510 from the first optical switch core 240. The sensed position signal is routed to the A/D converter 765, which is subsequently placed in a digital format before routing to the second processing unit 755. When the servo module 225, is adjusting the switch fabric, the second processing unit 755 transfers mirror control signals to the D/A converter 770. The mirror control signals are routed to HV driver 785 and applied to a selected mirror of the first optical switch core in order to adjust the amount of torsional flexure along a first dimensional plane (e.g., X-axis). This is accomplished to minimize the loss experienced by the light signal.

A second hinge position sensor 781 senses a position of a mirror for the first optical switch core along a second dimensional plane (e.g., Y-axis). The sensed position signal is routed to the A/D converter 765, which is subsequently placed in a digital format before routing to the second processing unit 755. When the servo module 225, is adjusting the switch fabric, the second processing unit 755 transfers mirror control signals to the D/A converter 770. The mirror control signals are routed to HV driver 786 and are applied to the selected mirror of the first optical switch core in order to adjust the amount of torsional flexure along the second dimensional plane. The specifics of the hinge position sensors 780 and 781 are described in a PCT application entitled "Micromachined Members Coupled for Relative Rotation By Torsional Flexure Hinges" (International Publication No. WO 00/13210) published on or around March 9, 2000.

In another embodiment, when I/O port module 215, is the destination of a light signal, the second processing unit 755 receives information representative of the measured power sense signals associated with the optical tap signal 662 that has been analyzed by the first processing unit 710. The optical tap signal 662 is based on an output light signal being routed from an I/O port. In this situation, the third processing unit 775 receives information associated with the measured power sense signals from a source I/O port as reported by SCM 236 over link 580.

IV. Redundant Architecture of the Optical Cross-Connect Switching System

Referring now to Figure 12, a block diagram of an alternative embodiment of the architecture of the optical cross-connect switching system of Figure 1 is shown which includes redundant protection capabilities. Redundancy is desired in order to increase the reliability of such an optical cross-connect switching system. Aside from the I/O port modules, all other modules are duplicated to obtain the desired redundancy. Thus, it is necessary for light signals from a source I/O port module 215_s to be routed to a destination I/O port module 215_d through two optical paths, namely a primary optical path 800 using a first optical switch core 240 and a redundant optical path 810 using a second optical switch core 260.

With respect to the primary optical path 800, a servo module 225_i is connected to both the source I/O port module 215_s and the first optical switch matrix (not shown) of the first optical switch core 240. In particular, the servo module 225_i controls the physical orientation of a mirror of the first optical switch matrix that corresponds to the source I/O port module 215_s. To establish and maintain the primary optical path 800 for the light signal, the servo module 225_i needs to communicate with other servo modules such as servo module 225_j. Thus, a servo control module (SCM) is implemented to support such communications, possibly through a time-slot switching arrangement.

As shown, the SCMs 236₁-236₂ are also duplicated so that each servo module 225 is connected to at least two SCMs 236₁-236₂. Thus, in the event that the SCM 236₁ fails, the primary optical path 800 remains intact because communications between the servo modules 225_i and 225_j are maintained via redundant SCM 237₁. The transfer is accomplished by temporarily halting the adjustment of (i.e. freezing) the mirrors inside the first optical switch core 240 while control is transferred from SCM 236₁ to SCM 237₁. The SCMs 236₁ and 237₁ associated with the first optical switch core 240 are in communication via a network control modules (NCMs) 238₁ and 238₂ for example.

With respect to the redundant optical path 810, a servo module 225_{i+1} is connected to both the source I/O port module 215_s and one or more mirror(s) of a

first optical switch matrix (not shown) of the second optical switch core 260.

Another servo module 225_{j+1} is connected to both the destination I/O port module 215_d and one or more mirror(s) of a second optical switch matrix (not shown) of the second optical switch core 260. The orientation of these mirrors produces the
5 redundant optical path 810.

To establish and maintain the redundant optical path 810 for the light signal, a SCM 236_2 may be implemented with a dedicated time-slot switching arrangement in order to support continuous communications between the servo module and another redundant servo module associated with the destination I/O
10 port module. As shown, the SCM 236_2 is also duplicated so that each servo module 225_{j+1} and 225_{j+1} is connected to at least two SCMs 236_2 and 237_2 . Thus, the redundant optical path 810 is maintained even when one of the SCMs 236_2 and 237_2 fails. The SCMs 236_2 and 237_2 associated with the second optical switch core 260 communicate via the first NCM 238_1 and the second NCM 238_2 ,
15 respectively. The second NCM 238_2 is in communication with the first NCM 238_1 to allow all SCMs and servo modules to communicate for coordination of the primary optical path 800 and the redundant optical path 810.

While certain exemplary embodiments have been described and shown in the accompanying drawings, it is to be understood that such embodiments are
20 merely illustrative of and not restrictive of the broad invention, and that this invention not be limited to the specific constructions and arrangements shown and described, since various other modifications may occur to those ordinarily skilled in the art.

CLAIMS

What is claimed is:

- 1 1. A scalable, optical cross-connect switching system comprising:
2 an input/output (I/O) subsystem including a plurality of removable I/O
3 port modules each adapted to receive an incoming light signal and to produce at
4 least two outgoing light signals based on the incoming light signal;
5 a switch subsystem to route each outgoing light signal over different
6 optical paths; and
7 a switch control subsystem coupled to the switch subsystem and the I/O
8 subsystem, the switch control subsystem to adjust the optical paths provided by
9 the switch subsystem.
- 1 2. The optical cross-connect switching system of claim 1, wherein the
2 switch subsystem includes a plurality of removable optical switches.
- 1 3. The optical cross-connect switching system of claim 2, wherein the
2 plurality of optical switches of the switch subsystem are a plurality of micro-
3 machined mirrors.
- 1 4. The optical cross-connect switching system of claim 1, wherein the
2 switch subsystem includes (1) a first optical switch core including a first plurality
3 of micro-machined mirrors to route a first outgoing light signal of the at least two
4 outgoing light signals over a first optical path and (2) a second optical switch core
5 including a second plurality of micro-machined mirrors to route a second
6 outgoing light signal of the at least two outgoing light signals over a second
7 optical path.
- 1 5. The optical cross-connect switching system of claim 4, wherein
2 one of the first and second optical switch cores can be replaced without disrupting

3 operations of the optical cross-connect switching system.

1 6. The optical cross-connect switching system of claim 4, wherein a
2 power level of the first outgoing light signal is substantially equivalent to a power
3 level of the second outgoing light signal.

1 7. The optical cross-connect switching system of claim 4, wherein the
2 I/O subsystem includes a source I/O port module and a destination I/O port
3 module, the source I/O port module to (a) perform a bridging operation on the
4 incoming light signal for producing the at least two outgoing light signals and (b)
5 provide an optical tap signal to the switching control subsystem.

1 8. The optical cross-connect switching system of claim 7, wherein the
2 switch control subsystem includes a first servo module coupled to the source I/O
3 port module and the first optical switch core, the first servo module to receive the
4 optical tap signal from the source I/O port module and to adjust the first plurality
5 of micro-machined mirrors of the first optical switch core in response to
6 information provided by the optical tap signal so as to reflect the first outgoing
7 light signal along the first optical path.

1 9. The optical cross-connect switching system of claim 8, wherein the
2 switch control subsystem further includes a second servo module coupled to the
3 first optical switch core and the destination I/O port module, the second servo
4 module to receive an optical tap signal from the destination I/O port module and
5 to operate in cooperation with the first servo module to adjust the first plurality of
6 micro-machined mirrors to reflect the first outgoing light signal along the first
7 optical path.

1 10. The optical cross-connect switching system of claim 9, wherein the
2 switch control subsystem further includes a third servo module coupled to the

3 second optical switch core and the source I/O port module, the third servo module
4 to receive a second optical tap signal from the source I/O port module and to
5 adjust the second plurality of micro-machined mirrors of the second optical switch
6 core in response to information provided by the second optical tap signal so as to
7 reflect the second outgoing light signal along the second optical path.

1 11. The optical cross-connect switching system of claim 10, wherein
2 the switch control subsystem further includes a fourth servo module coupled to
3 the second optical switch core and the destination I/O port module, the fourth
4 servo module to receive a second optical tap signal from the destination I/O port
5 module and to operate in cooperation with the third servo module to adjust the
6 second plurality of micro-machined mirrors to reflect the second outgoing light
7 signal along the second optical path.

1 12. The optical cross-connect switching system of claim 9, wherein the
2 switch control subsystem further includes a servo control module coupled to at
3 least the first servo module and the second servo module, the servo control
4 module to enable the first servo module to communicate with the second servo
5 module.

1 13. The optical cross-connect switching system of claim 12, wherein
2 the switch control subsystem further includes a redundant servo control module
3 coupled to the first servo module and the second servo module, the redundant
4 servo control module to enable the first and second servo modules to
5 communicate in the event that the first servo control module is non-operational.

1 14. The optical cross-connect switching system of claim 12, wherein
2 the switch control subsystem further includes a network control module coupled
3 to the first servo control module associated with the first servo module and the
4 second servo modules and a second servo control module associated with a third

5 servo module and a fourth servo module, the network control module to enable
6 the first and second servo control modules and the first, second, third and fourth
7 servo modules to communicate for adjustment of the first optical path and the
8 second optical path.

1 15. The optical cross-connect switching system of claim 1, wherein the
2 switch control subsystem includes a servo module coupled to a plurality of tap
3 couplers for a first I/O port module, the servo module to receive an optical tap
4 signal from one of the plurality of tap couplers and to inject a substitute light
5 signal back to the plurality of tap couplers in response to information provided by
6 the optical tap signal.

1 16. The optical cross-connect switching system of claim 15, wherein
2 the servo module of the switching control subsystem includes a laser for injecting
3 of the substitute light signal back to the plurality of tap couplers.

1 17. The optical cross-connect switching system of claim 15, wherein
2 the substitute light is within the same wavelength range as the incoming light
3 signal.

1 18. An input/output (I/O) port module comprising:
2 a substrate; and
3 a data propagation circuit mounted on the substrate, the data propagation
4 circuit to (1) receive an incoming light signal, (2) produce at least two bridged
5 light signals collectively having a power level substantially equal to a power level
6 of the incoming light signal, and (3) output at least two outgoing light signals
7 based on the at least two bridged light signals.

1 19. The I/O port module of claim 18, wherein the substrate is a printed
2 circuit board including a first interface to receive the incoming light signal over a
3 plurality of optical fiber links.

1 20. The I/O port module of claim 18, wherein the data propagation
2 circuit includes a passive splitter that produces the at least two bridged light
3 signals.

1 21. The I/O port module of claim 20, wherein the at least two bridged
2 light signals produced by the passive splitter have substantially equal power
3 levels.

1 22. The I/O port module of claim 20, wherein the at least two bridged
2 light signals produced by the passive splitter have substantially disproportionate
3 power levels.

1 23. The I/O port module of claim 20, wherein the data propagation
2 circuit further includes at least two tap couplers coupled to the passive splitter,
3 each tap coupler to separate one of the at least two bridged light signals into one
4 of the at least two outgoing light signals and an optical tap signal for use in
5 monitoring a power level of the one of the at least two outgoing light signals.

1 24. The I/O port module of claim 19, wherein the substrate includes a
2 second interface to receive at least two light signals over a plurality of optical
3 fiber links from an optical switch fabric.

1 25. The I/O port module of claim 24, wherein the data propagation
2 circuit further includes an optical switch to select one of the at least two light
3 signals received over the second interface for output via the first interface.

1 26. The I/O port module of claim 24 further comprising at least one
2 additional data propagation circuit communicatively coupled to the first interface
3 and the second interface, the data propagation circuit and the at least one
4 additional data propagation circuit correspond to a number of ports associated
5 with the I/O port module.

1 27. The I/O port module of claim 25 further comprising a control
2 circuit to provide control signals for selection of one of the at least two light
3 signals for output from the optical switch.

1 28. The I/O port module of claim 27, wherein the control circuit
2 further receives light signal quality information pertaining to the at least two light
3 signals from modules situated remotely from the I/O port module.

1 29. The I/O port module of claim 27, wherein the control circuit
2 further comprises
3 a processing unit; and
4 a programmable memory coupled to the processing unit, the
5 programmable memory to contain software executable by the processing unit for
6 providing the control signals.

1 30. The I/O port module of claim 29, wherein the programmable
2 memory of the control circuit contains information used by the processing unit to
3 control a state of a plurality of status identification component to identify that the
4 I/O port module is operational and which ports of the I/O port module are
5 operational.

1 31. A servo module comprising:
2 a servo mirror control module configure for adjusting an optical path of a

3 light signal from a source to a destination; and
4 an optical detector module in communication with and removably coupled
5 to the servo mirror control module, the optical detector module to monitor a
6 power level of the light signal to determine whether to adjust the optical path.

1 32. The servo module of claim 31, wherein disconnection of the
2 optical detector module from the servo mirror control module causes the servo
3 mirror control module to halts adjustment of the optical path until the optical
4 detector module is reconnected to the servo mirror control module.

1 33. The servo module of claim 31, wherein the optical detector module
2 comprises:
3 a first processing unit;
4 a plurality of detection/modulation (DM) circuits coupled to the first
5 processing unit, each DM circuit to convert an optical tap signal adapted for
6 sensing a power level of the light signal at the source into a first measured power
7 sense signal that can be analyzed by the first processing unit.

1 34. The servo module of claim 33, wherein at least one DM circuit
2 comprises:
3 a first optical-to-electrical (O/E) detector to convert the optical tap signal
4 into an electrical control signal; and
5 an analog-to-digital converter to perform a digital conversion of the
6 electrical control signal to produce the first measured power sense signal.

1 35. The servo module of claim 34, wherein the optical detector module
2 further comprises an amplifier coupled to both the O/E detector and the analog-to-
3 digital converter to amplify the electrical control signal.

1 36. The servo module of claim 33, wherein the servo mirror control

2 module comprises:

3 a second processing unit coupled to the first processing unit; and
4 a plurality of mirror signal detection and generation (SDG) circuits
5 coupled to and controlled by the second processing unit.

1 37. The servo module of claim 36, wherein each SDG circuit includes
2 an analog-to-digital converter;
3 a digital-to-analog converter;
4 a plurality of hinge position sensors coupled to the analog-to-digital
5 converter; and
6 a plurality of high voltage drivers coupled to the digital-to-analog
7 converter.

1 38. The servo module of claim 36 further comprising a third
2 processing unit coupled to the second processing unit, the third processing unit to
3 provide a second measured power sense signal associated with the light signal at
4 the destination.

1 39. The servo module of claim 33, wherein the DM circuit further
2 comprises a laser to inject a substitute light signal into the optical path.

1 40. The servo module of claim 39, wherein the substitute light is
2 within the same wavelength range as the light signal.

1 41. A method of transferring optical signals in a scalable cross-connect
2 switching system, the method comprising:
3 receiving an incoming light signal;
4 producing a first outgoing light signal and a second outgoing light signal
5 based on the incoming light signal;
6 routing the first outgoing light signal through a first optical switch core

7 between a source input/output (I/O) port module and a destination I/O port
8 module; and
9 routing of the second outgoing light signal through a second optical switch
10 core between the source I/O port module and the destination I/O port module.

1 42. The method of claim 41, wherein the producing of the first
2 outgoing light signal and the second outgoing light signal comprises:
3 separating the incoming light signal into a first bridged light signal and a
4 second bridged light signal; and
5 separating the first bridged light signal into a first optical tap signal and
6 the first outgoing light signal, the first optical tap signal is used for controlling the
7 first optical switch core to improve integrity of the first outgoing light signal.

1 43. The method of claim 42, wherein the producing of the first
2 outgoing light signal and the second outgoing light signal further comprises
3 separating the second bridged light signal into a second optical tap signal and the
4 second outgoing light signal, the second optical tap signal being used for
5 controlling the second optical switch core to improve integrity of the second
6 outgoing light signal.

1 44. The method of claim 42, wherein a power level of the first optical
2 tap signal is less than a power level of the first outgoing light signal.

1 45. The method of claim 43, wherein a power level of the second
2 optical tap signal is less than a power level of the second outgoing light signal.

1 46. An optical cross-connect switching system comprising:
2 a switch subsystem including a plurality of removable optical switch
3 cores;
4 an input/output (I/O) port module in communication with the switch

5 subsystem, the I/O port module including at each port:
6 a splitter to produce at least two bridge signals from an incoming
7 light signal,
8 a first tap coupler communicatively coupled to the splitter, the first
9 tap coupler to produce a first outgoing light signal routed to the switch
10 subsystem and a first optical tap signal, and
11 a second tap coupler communicatively coupled to the splitter, the
12 second tap coupler to produce a second outgoing light signal routed to the
13 switch subsystem and a second optical tap signal; and
14 a servo module in communication with the I/O module, the servo module
15 to receive the first optical tap signal and to control the switch subsystem based on
16 information provided by the first optical tap signal.

1 47. The optical cross-connect switching system of claim 46, wherein
2 the I/O port module further comprises:
3 an optical switch; and
4 a control circuit coupled to the optical switch and the servo module, the
5 control circuit to control operations of the optical switch.

1 48. The optical cross-connect switching system of claim 46, wherein
2 the servo module further comprises an optical detector module and a servo mirror
3 control module.

1 49. The optical cross-connect switching system of claim 48, wherein
2 the servo mirror control module includes logic to adjust mirror orientations within
3 the switch subsystem in response to measured power sense signals from the
4 optical detector module.

1 50. The optical cross-connect switching system of claim 48, wherein
2 the optical detector module to receive the first optical tap signal, convert the first

3 optical tap signal into an electrical control signal, amplify the electrical control
4 signal, and digitize the electrical control signal to produce the measured power
5 sense signals.

1 51. The optical cross-connect switching system of claim 48, wherein
2 the optical detector module can be disconnected from the servo mirror control
3 module without disrupting operations of the optical cross-connect switching
4 system.

1 52. The optical cross-connect switching system of claim 51, wherein
2 the servo mirror control module halts further adjustment of the switch subsystem
3 until the optical detector module is reconnected to the servo mirror control
4 module.

1 53. The optical cross-connect switching system of claim 46, wherein
2 the servo module includes a laser to inject a substitute light signal back to the I/O
3 port module for integrity enhancement of the first outgoing light signal.

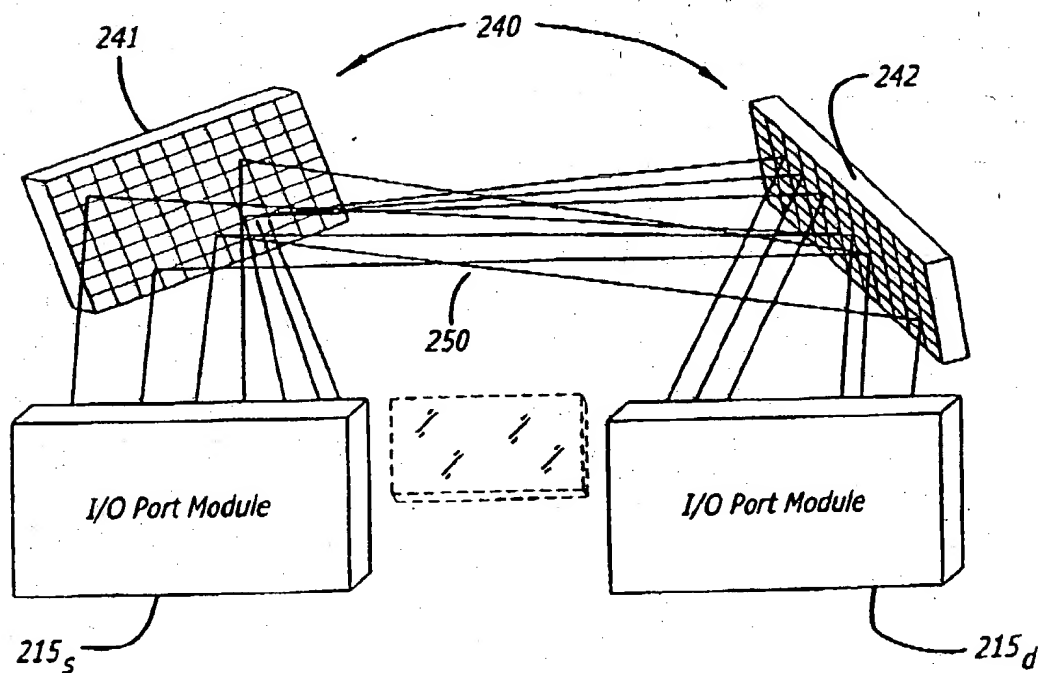
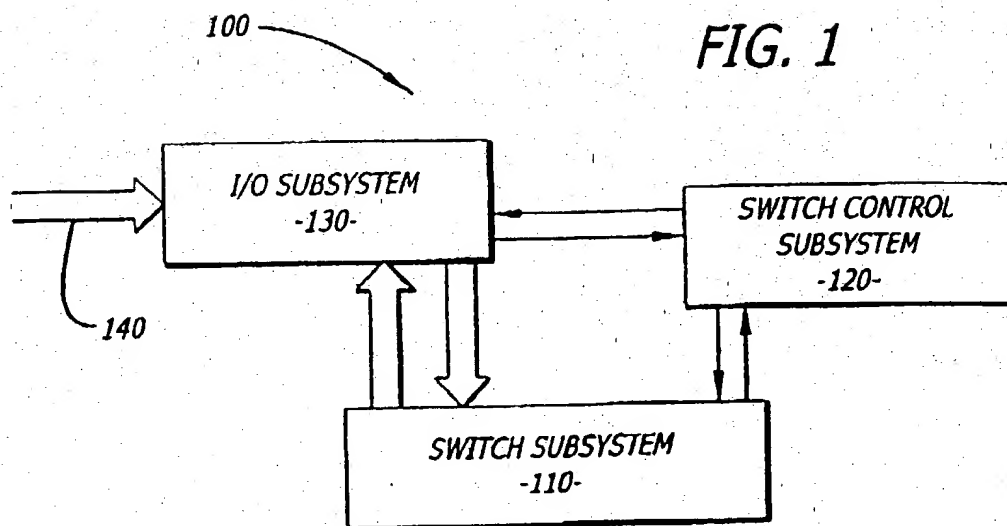
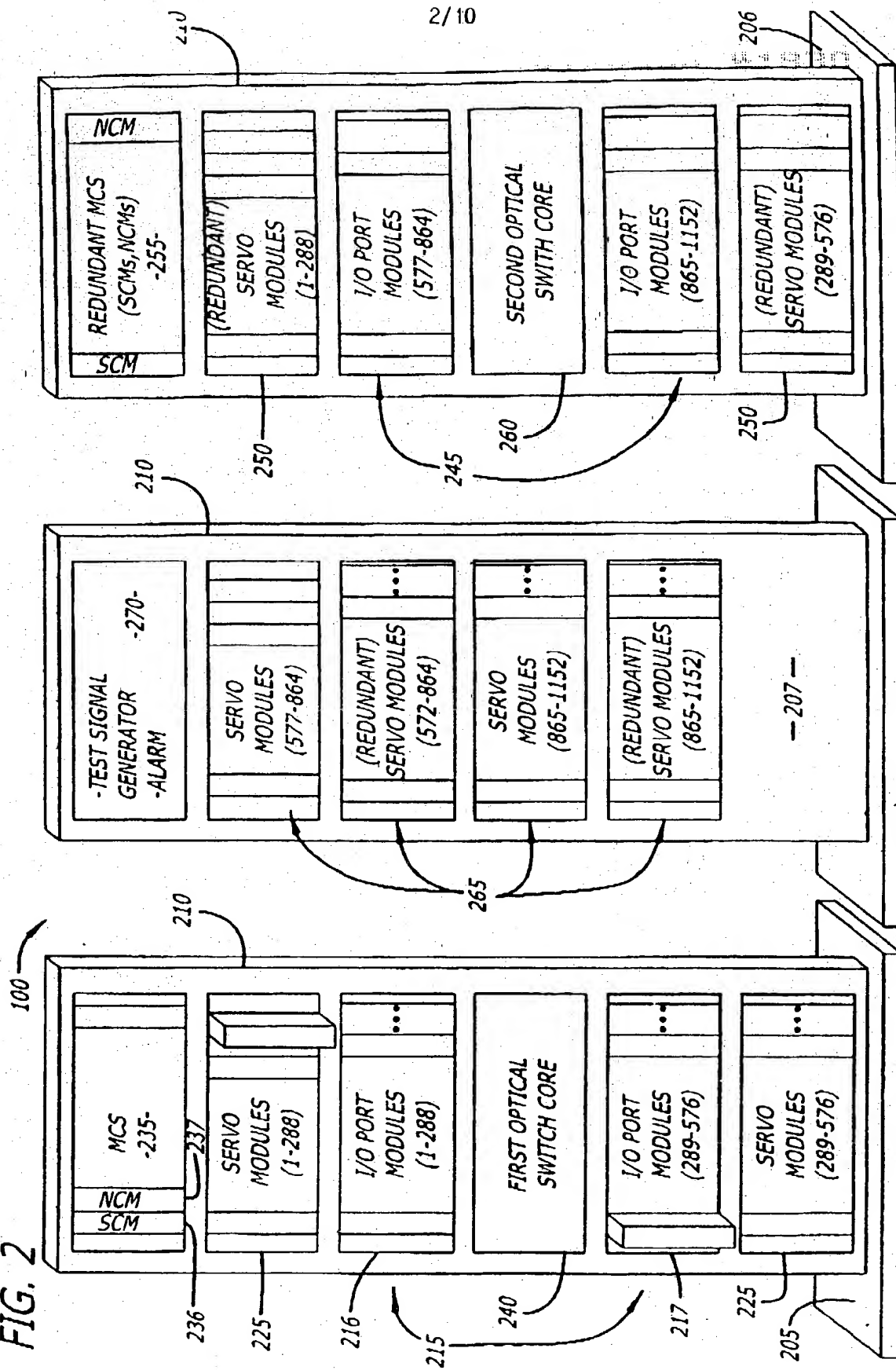
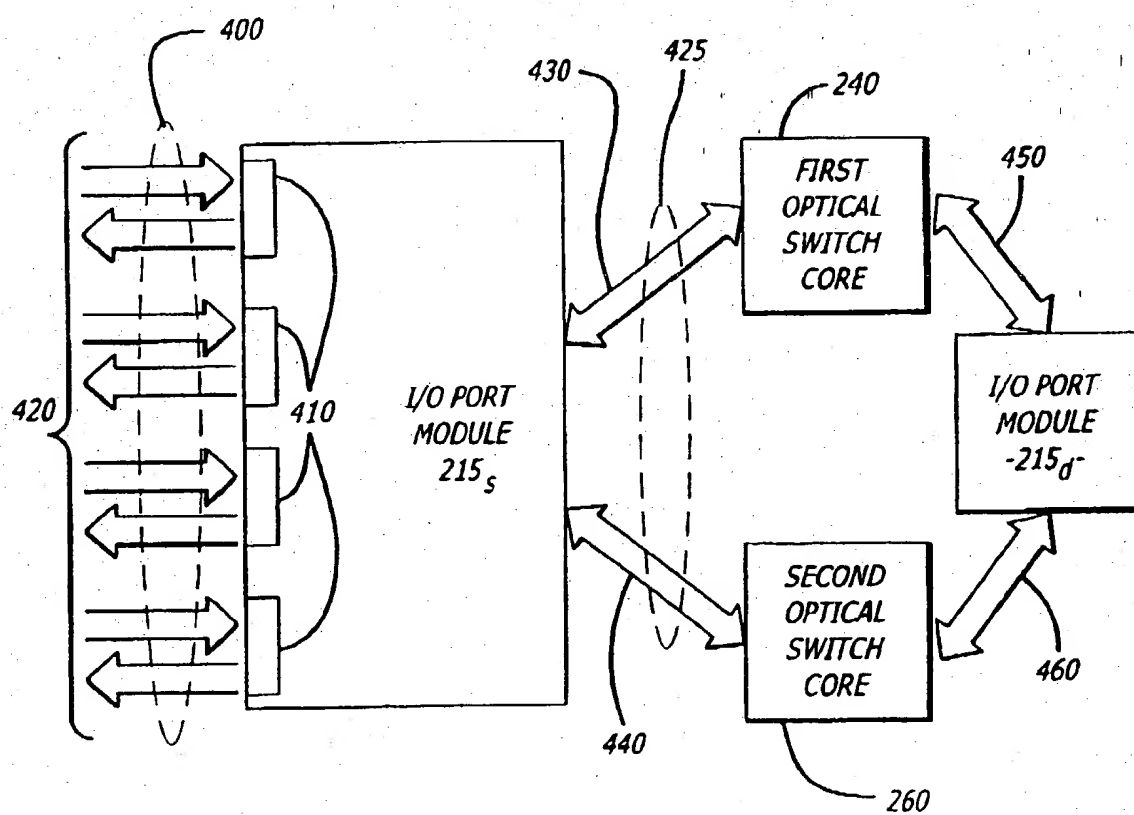
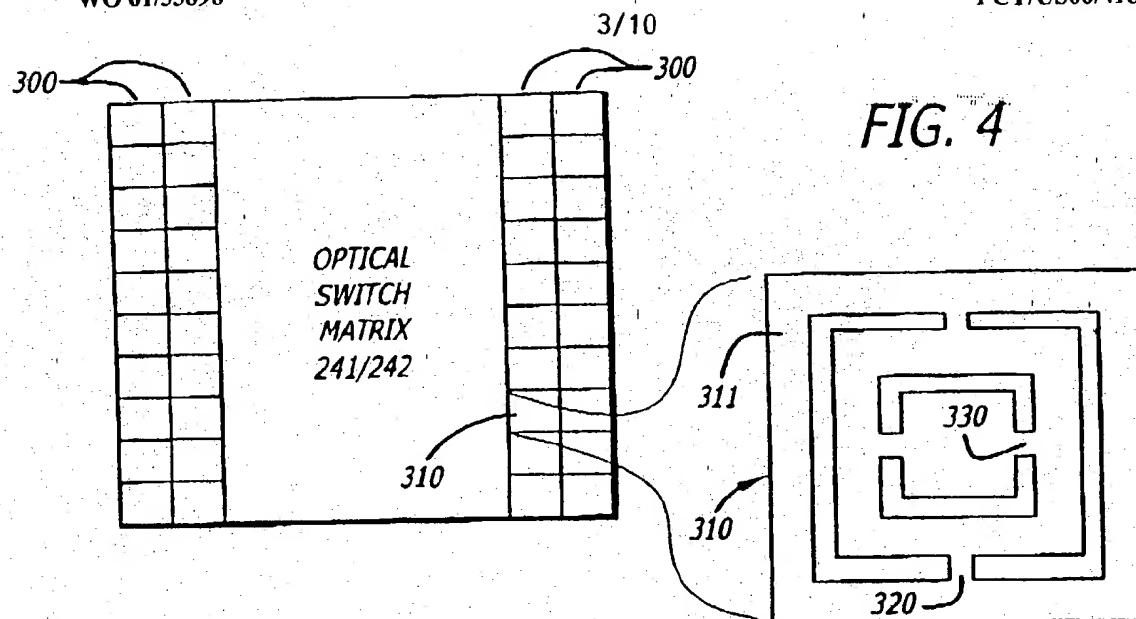


FIG. 2





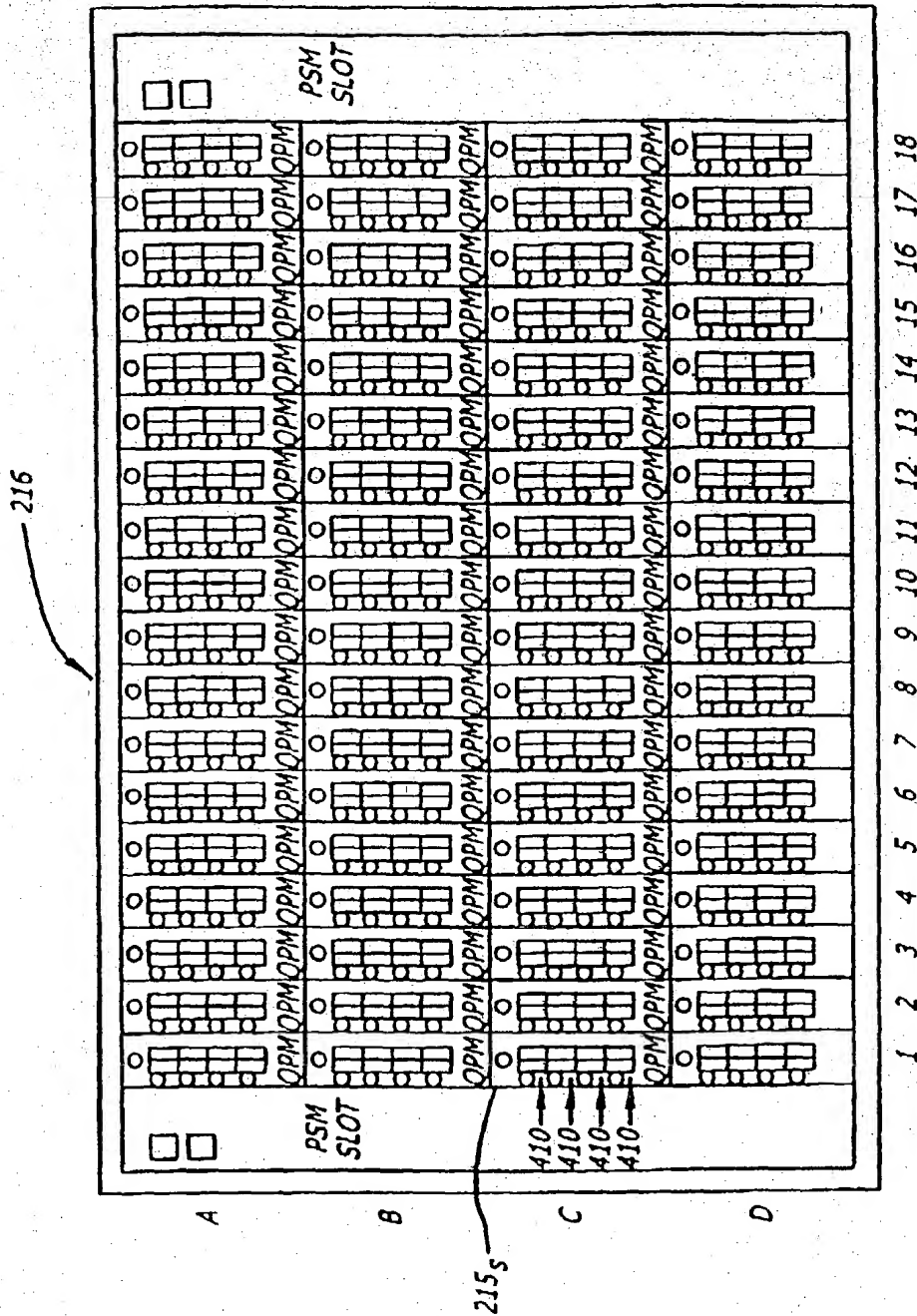
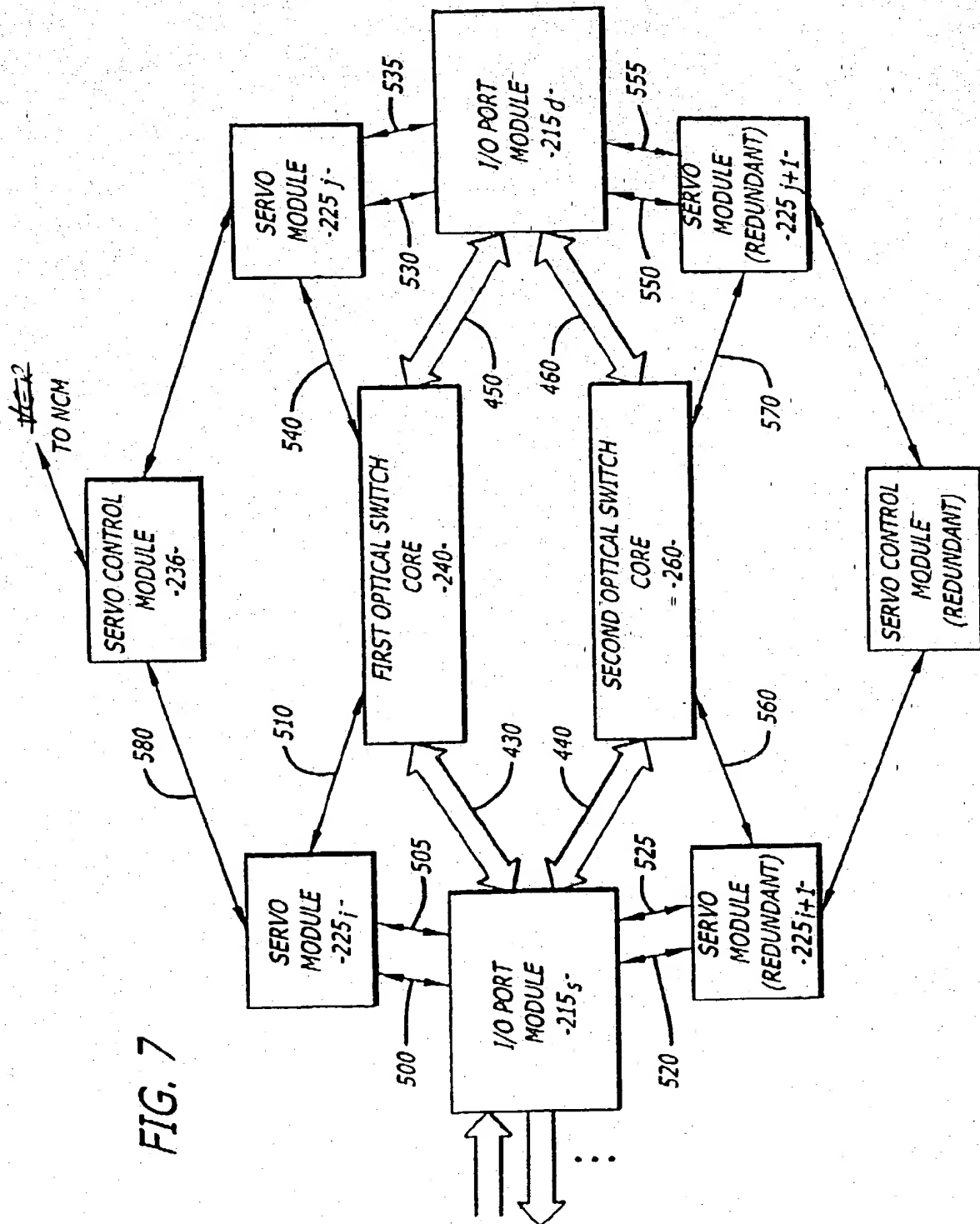
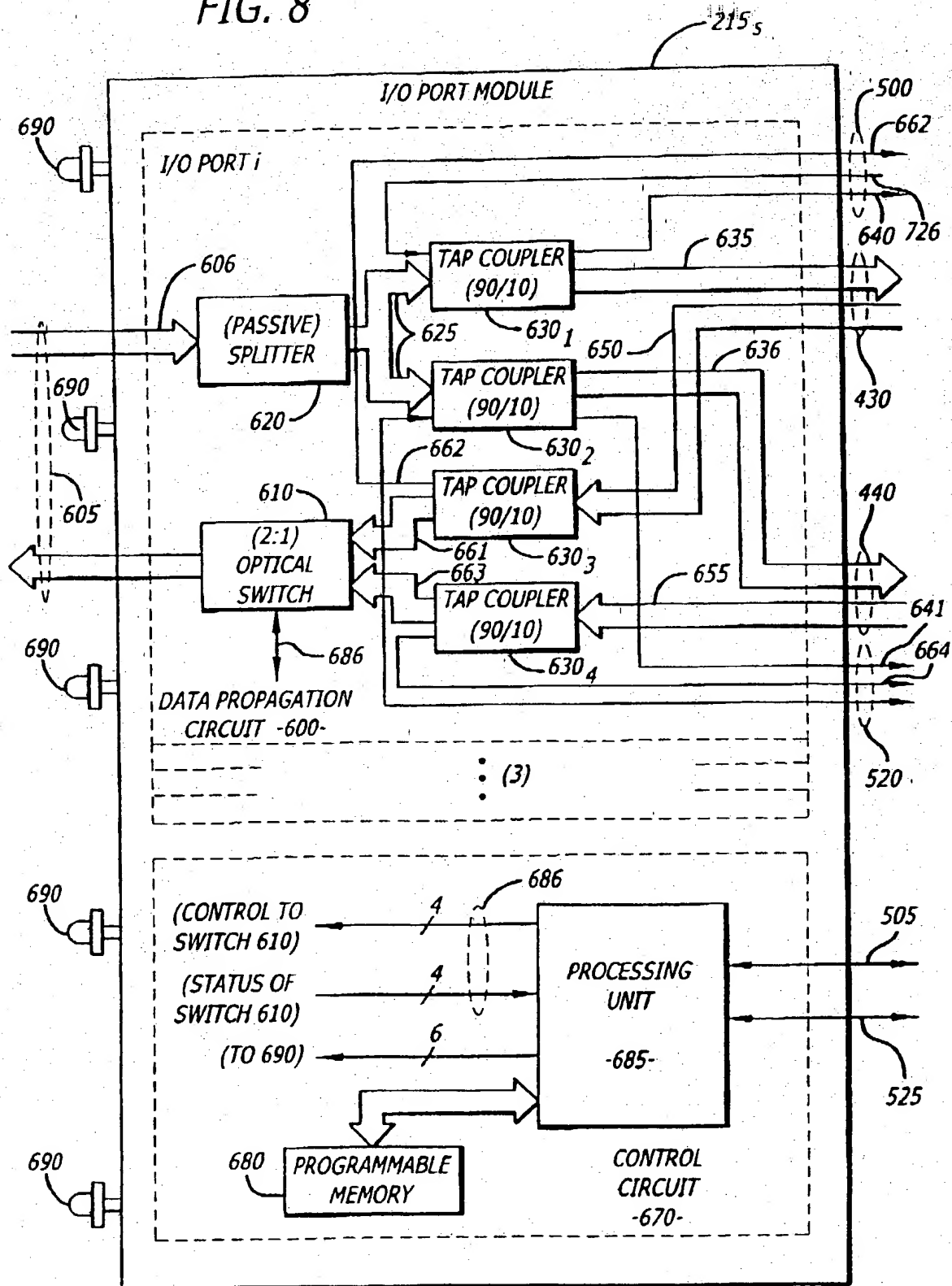


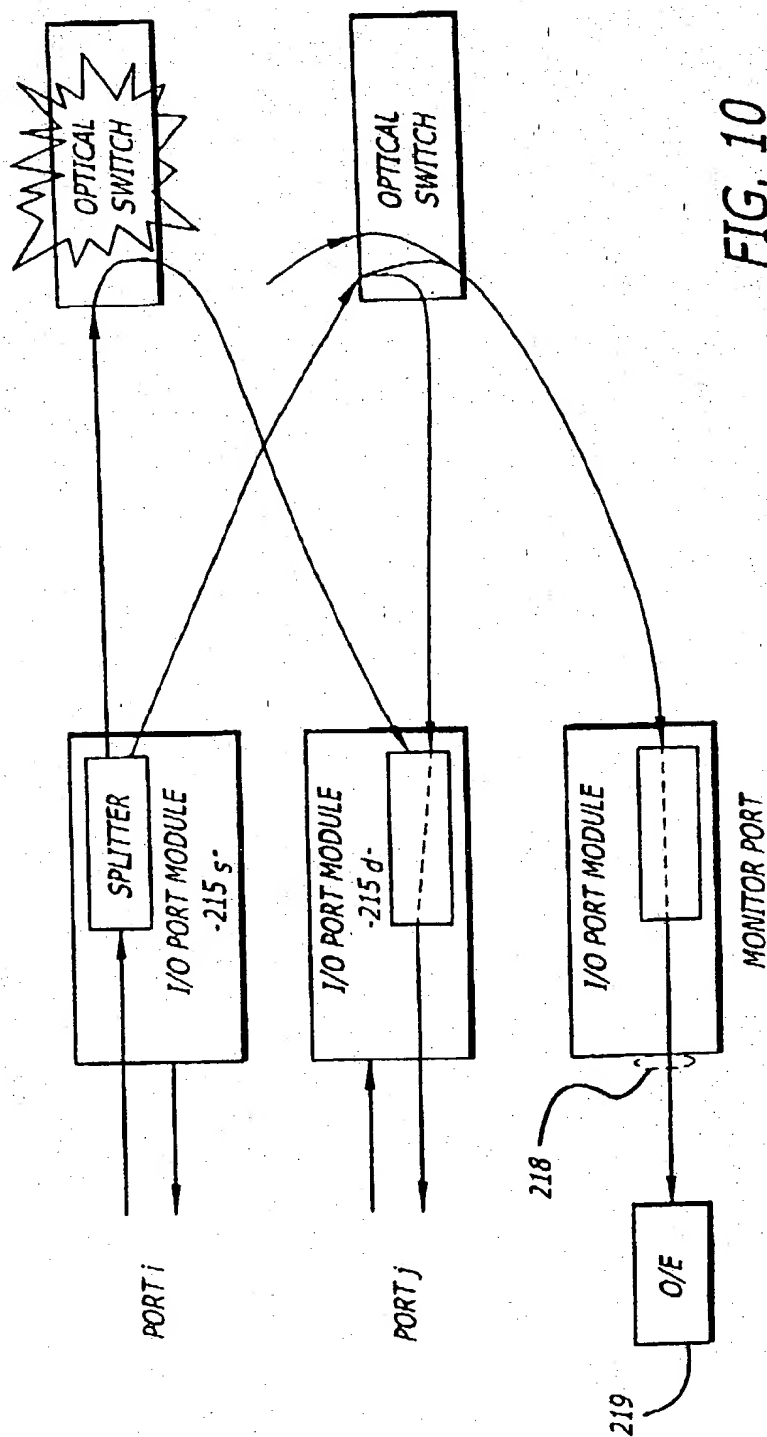
FIG. 5



6/10

FIG. 8





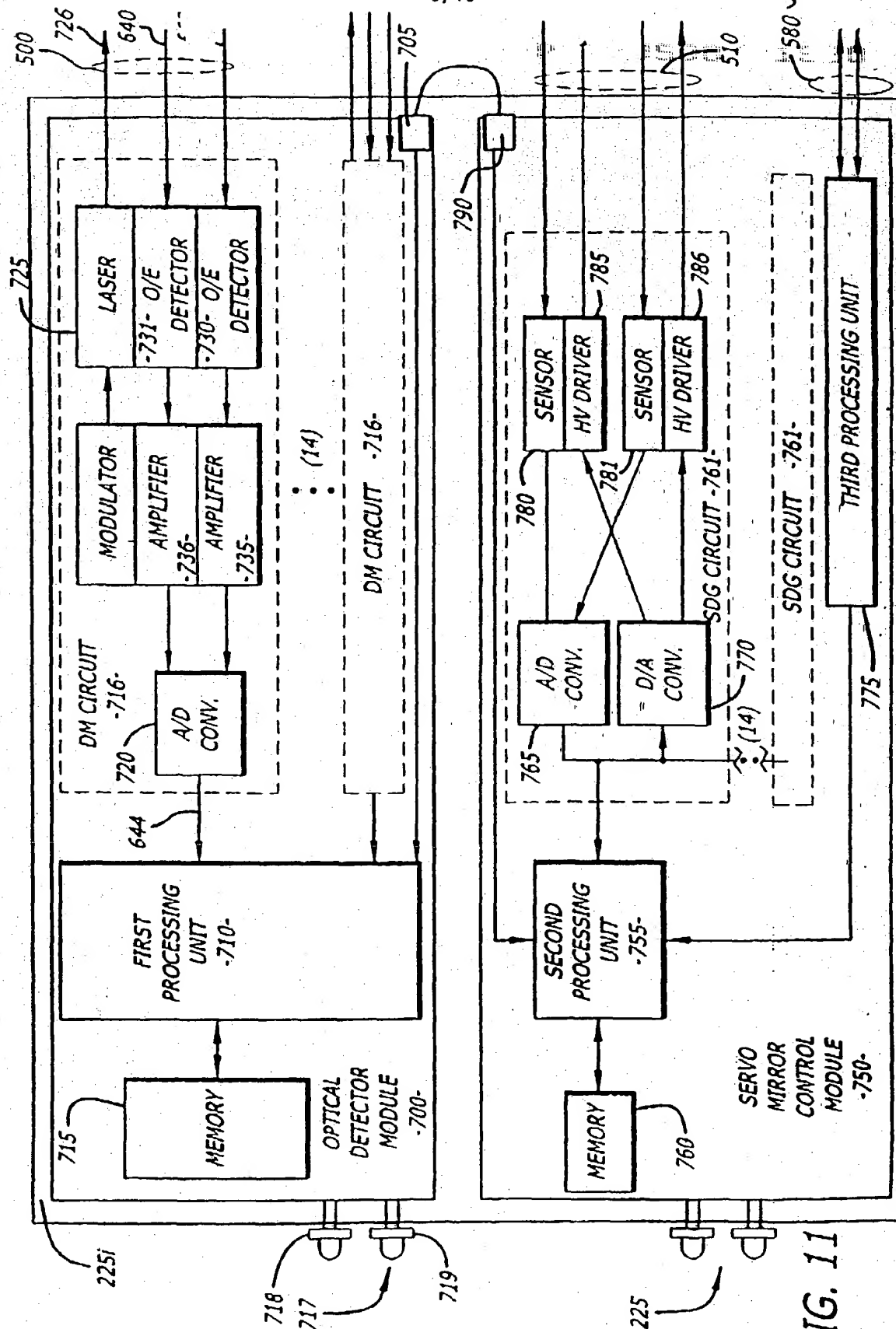


FIG. 11

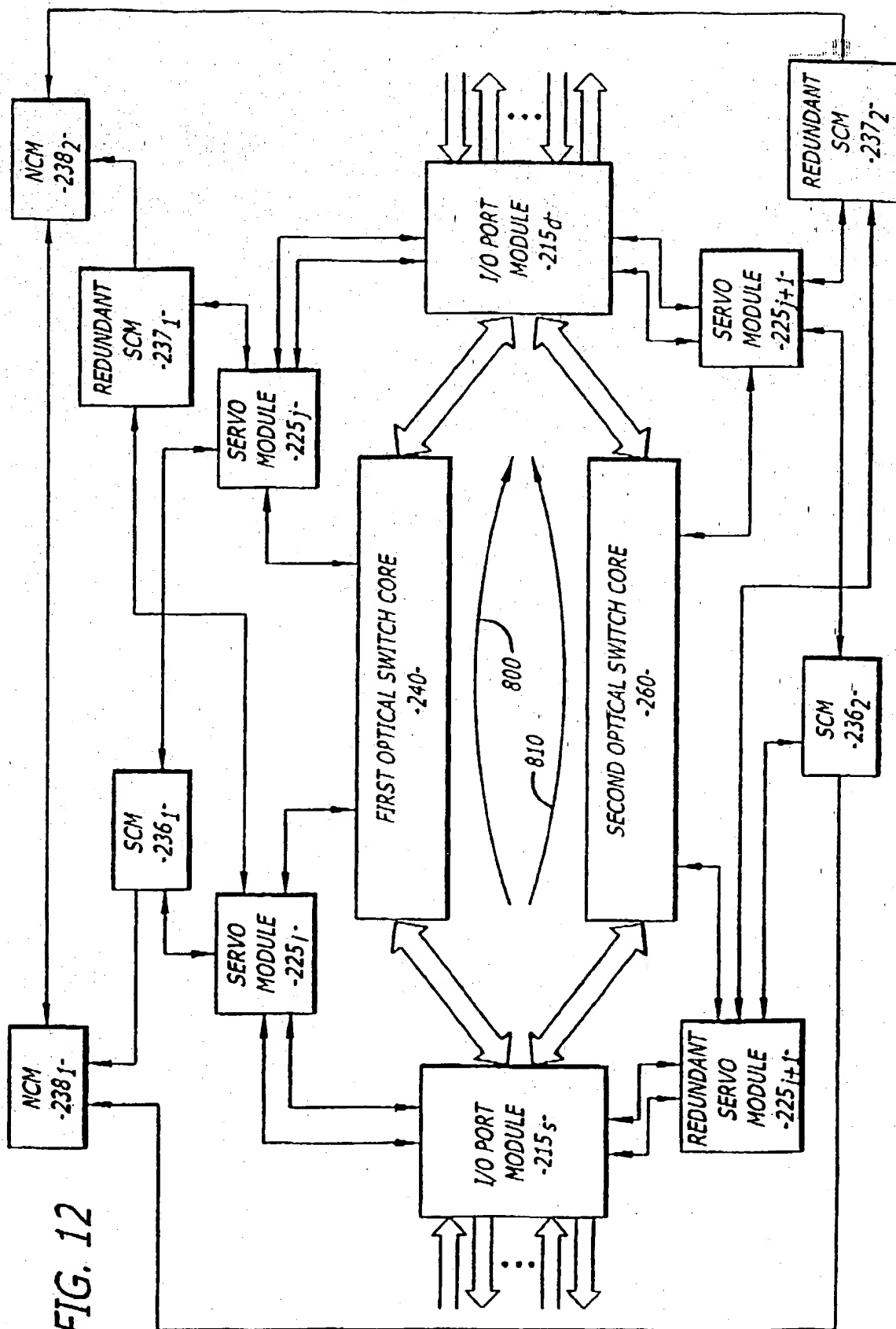


FIG. 12